

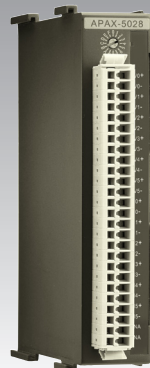
APAX-5028

APAX-5040

8-ch Analog Output Module

24-ch Digital Input Module

Preliminary



APAX-5028

Preliminary



APAX-5040

Specifications

General

- **Enclosure** ABS+PC
- **Power Consumption** 4.5W

Analog Output

- **Channels** 8
- **Output Type** V, mA
- **Output Range** ± 2.5 V, ± 5 V, ± 10 V, $0 \sim 2.5$ V, $0 \sim 5$ V, $0 \sim 10$ V, $0 \sim 20$ mA, $4 \sim 20$ mA
- **Resolution** 14-bit
- **Accuracy** $\pm 0.1\%$ of FSR
- **Settling time** about 500 μ s
- **Slew Rate** 0.0625 VDC/ μ s (per channel)
- **Span Drift** ± 60 ppm/ $^{\circ}$ C
- **Zero Drift** ± 275 mV/ $^{\circ}$ C (Voltage)
 ± 250 mV/ $^{\circ}$ C (Current)
- **Drive Voltage (Current)** 15 VDC
- **Load (Current Mode)** $0 \sim 500\Omega$

Protection

- **Isolation Between Channels and Backplane** 3000 VDC
- **Short Circuit Protection** Yes

Environment

- **Operating Temperature** $-10 \sim 50^{\circ}$ C
- **Storage Temperature** $-25 \sim 85^{\circ}$ C
- **Humidity** 5 ~ 95% (non-condensing)

Ordering Information

- **APAX-5028** 8-ch Analog Output Module

Specifications

General

- **Enclosure** ABS+PC
- **Power Consumption** 1.5W
- **Status Display** LED per channel
On: Logic level 1
Off: Logic level 0

Digital Input

- **Channels** 24
- **Points per Common** 12
- **Type** Sink (Wet Contact)
- **Input Voltage**
Rated Value 24 VDC
For logic level 0 $-30 \sim 5$ VDC
For logic level 1 13 ~ 30 VDC
- **Input Impedance** 10 k Ω
- **Input Delay** From logic level 0 to 1: 0.5 ms
From logic level 1 to 0: 0.5 ms
- **Operating Frequency** 3 kHz
- **Hardware Input Filter** 3 kHz

Protection

- **Over Voltage Protection** ± 35 VDC
- **Isolation Between Channels and Backplane** 3000 VDC

Environment

- **Operating Temperature** $-10 \sim 50^{\circ}$ C
- **Storage Temperature** $-25 \sim 85^{\circ}$ C
- **Humidity** 5 ~ 95% (non-condensing)

Ordering Information

- **APAX-5040** 24-ch Digital Input Module